

12204495

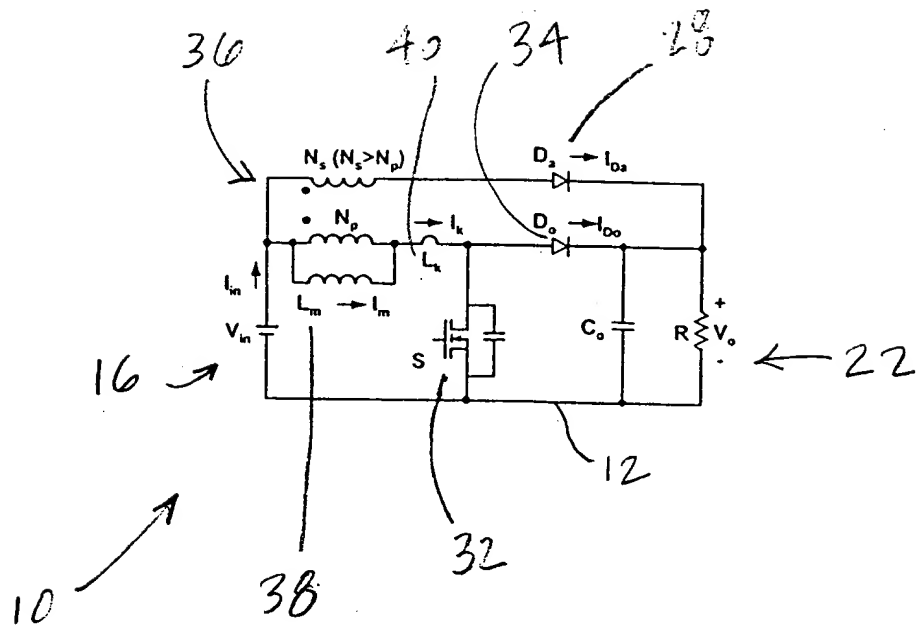


FIG. 2

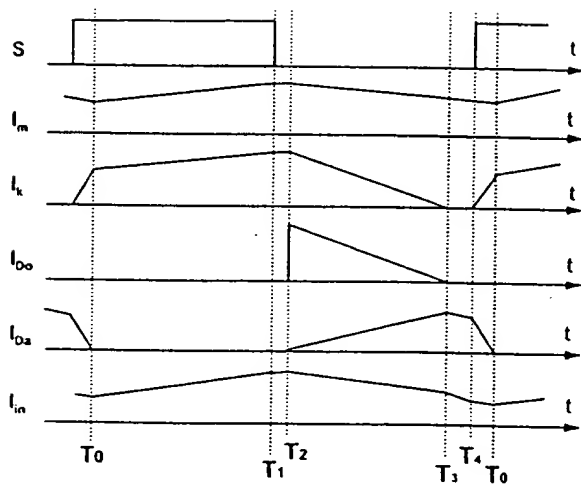
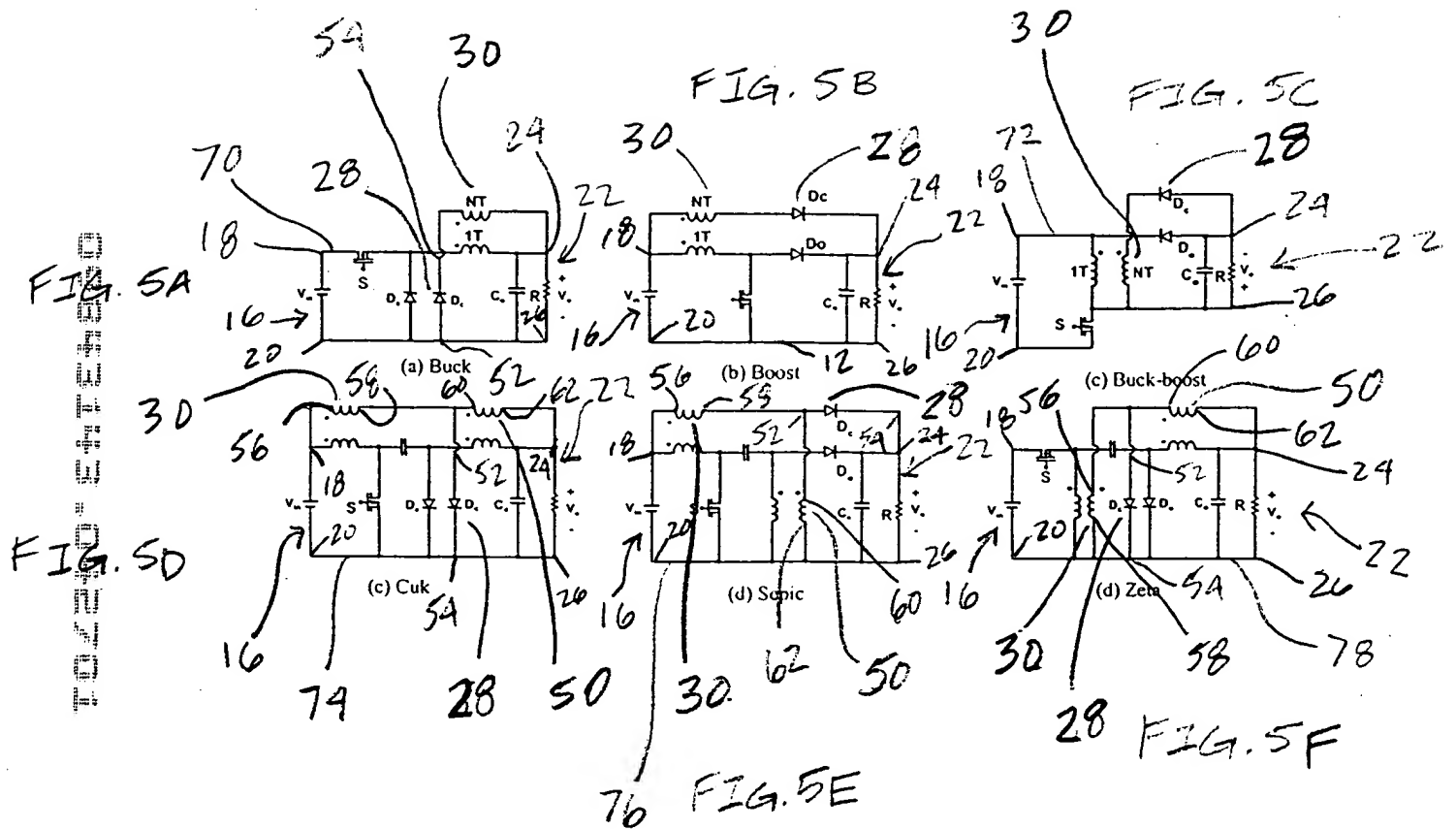


FIG.3



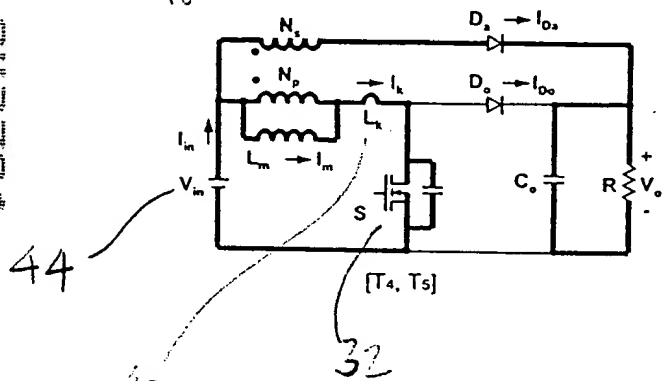
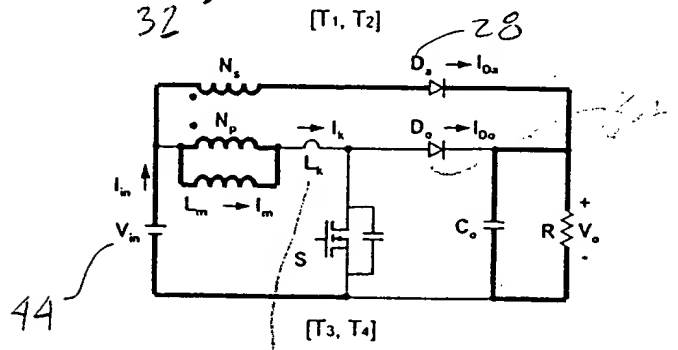
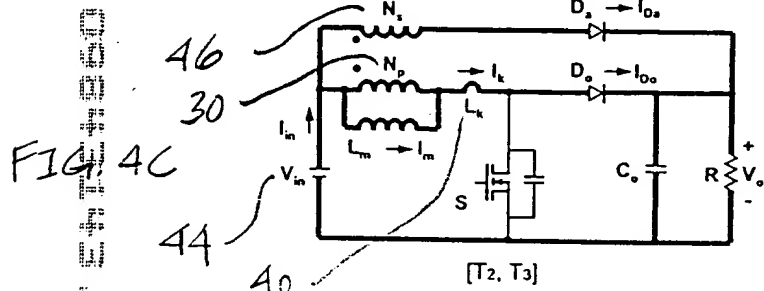
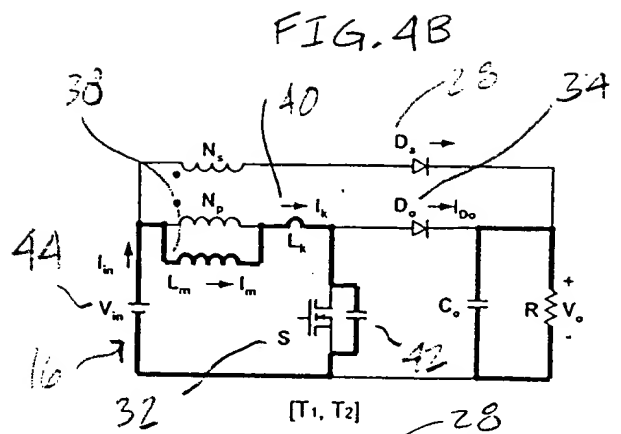
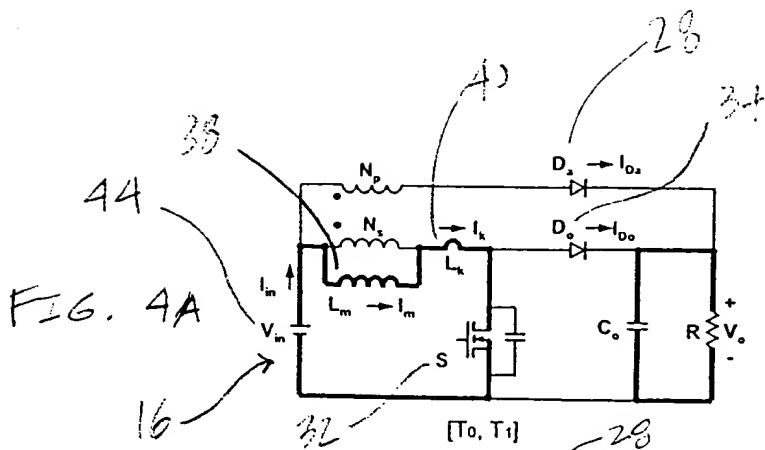


FIG. 4E

FIG. 4D

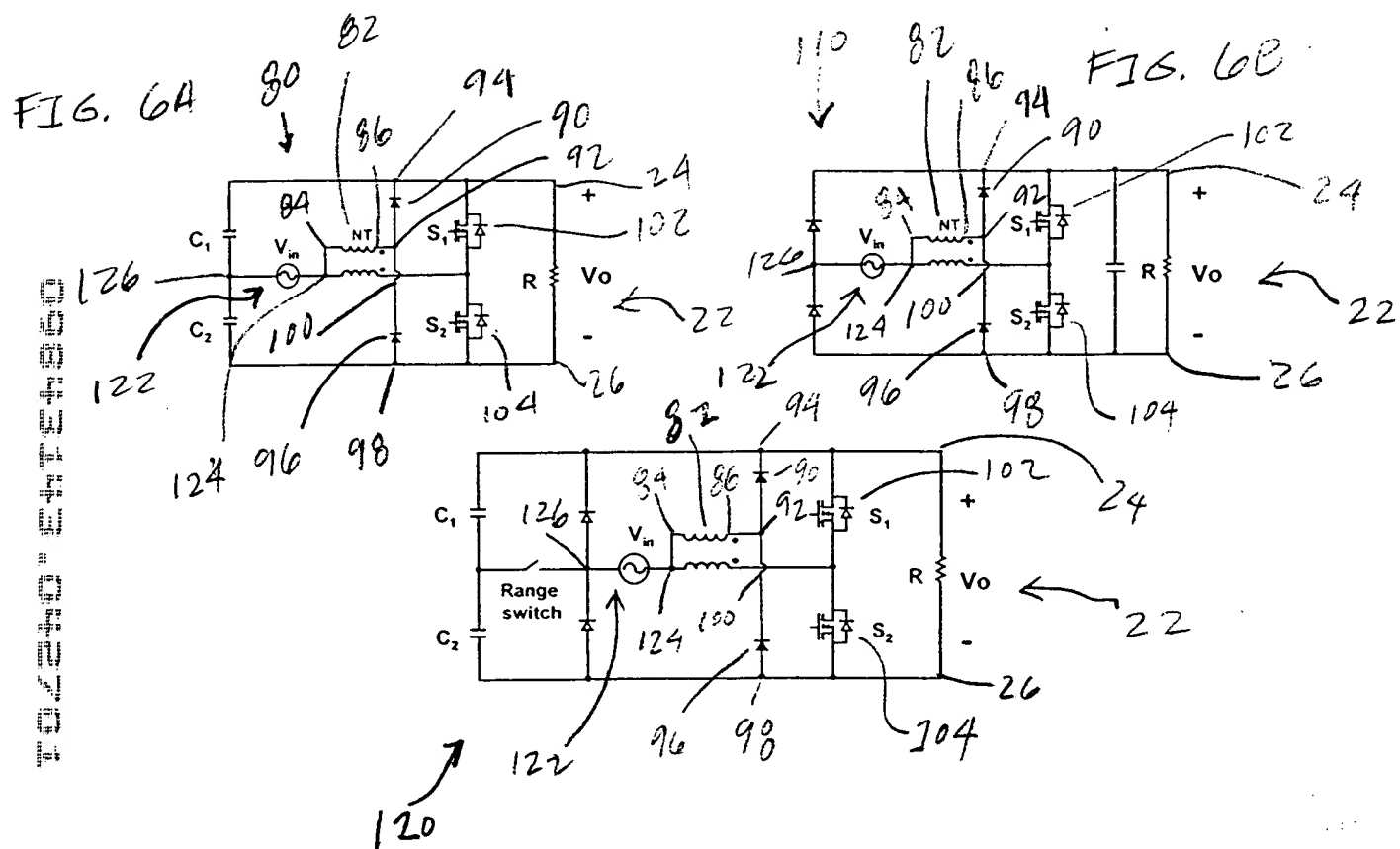


FIG 6C

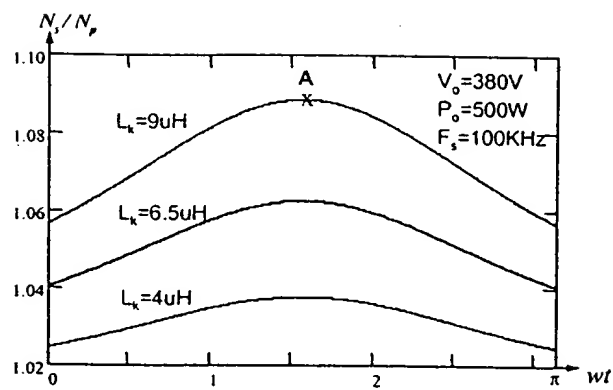


FIG. 7

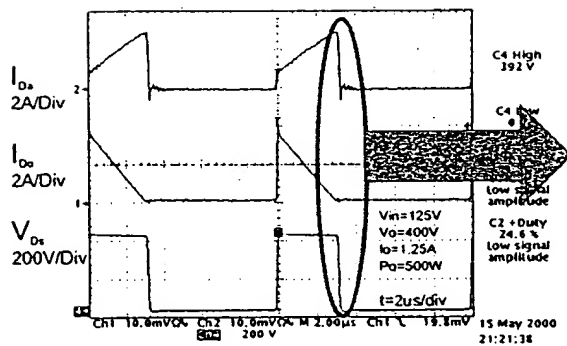


FIG. 9A

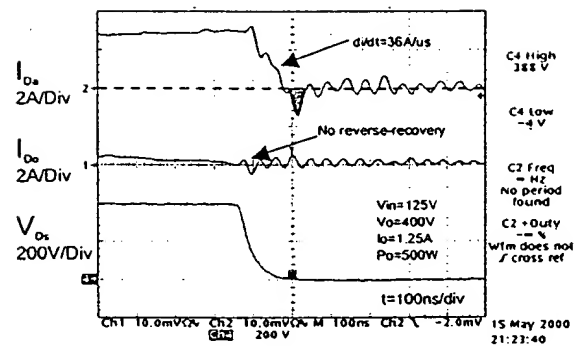


FIG. 9B

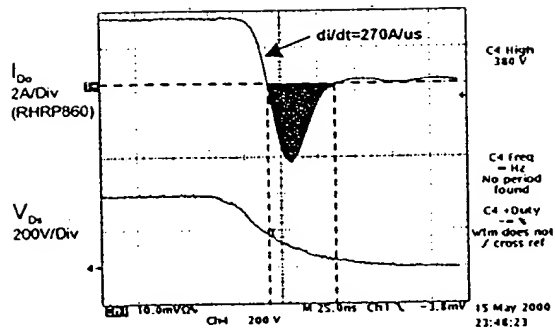


FIG. 10A
(PRIOR ART)

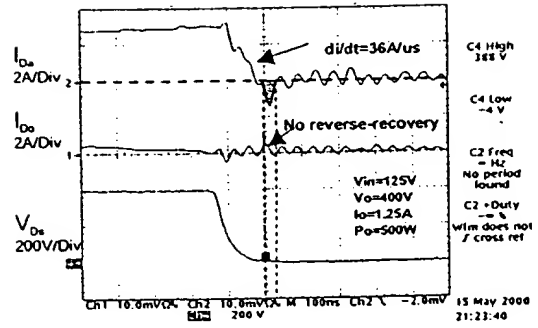


FIG. 10B

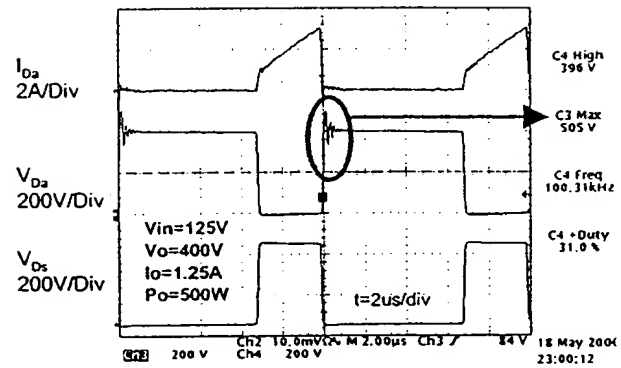


FIG. 11

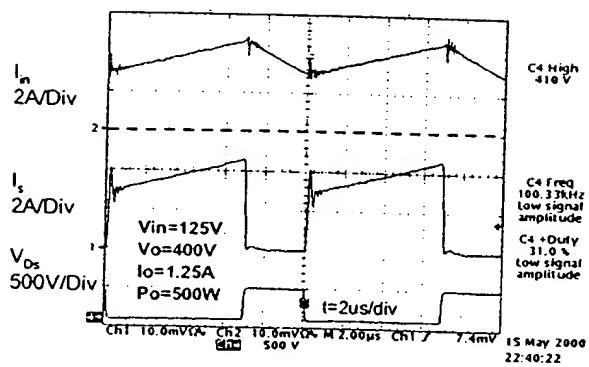


FIG. 12

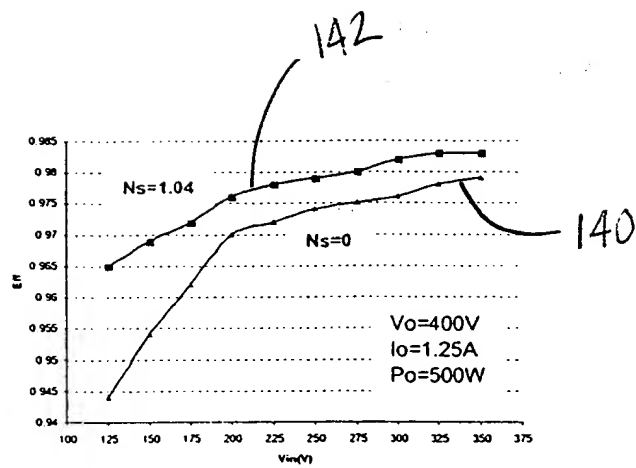


FIG. 13

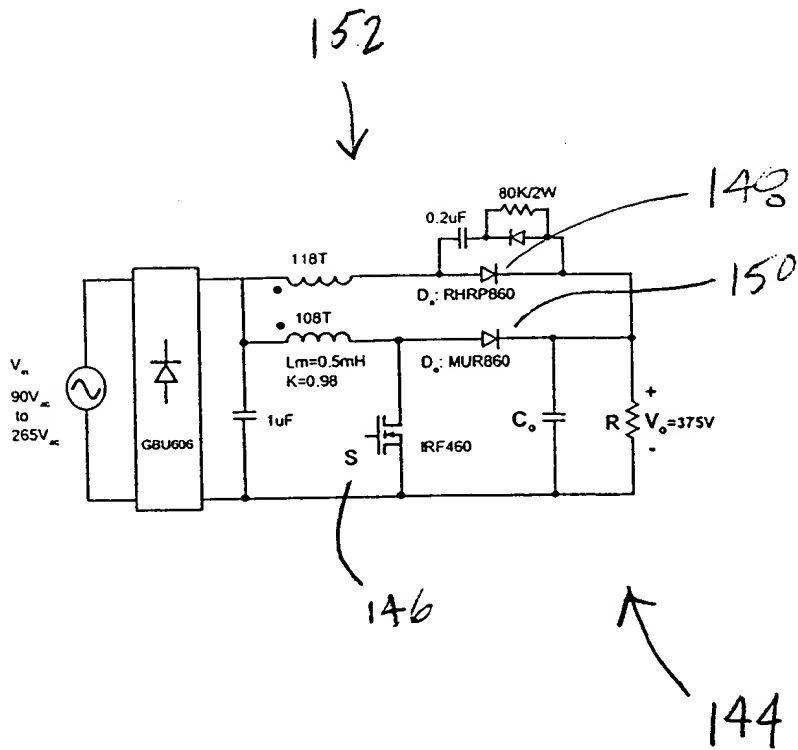


FIG. 14

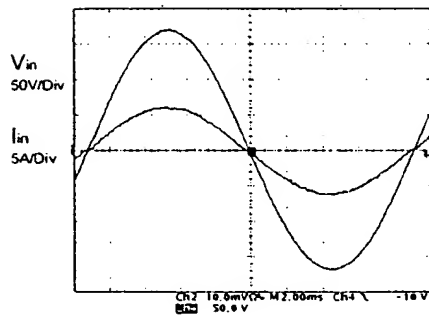


FIG. 15A

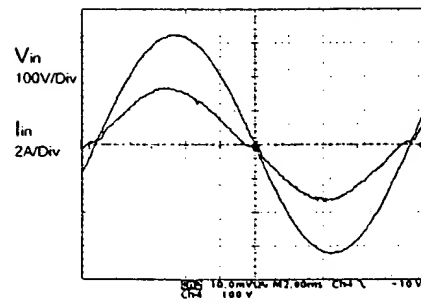


FIG. 15B

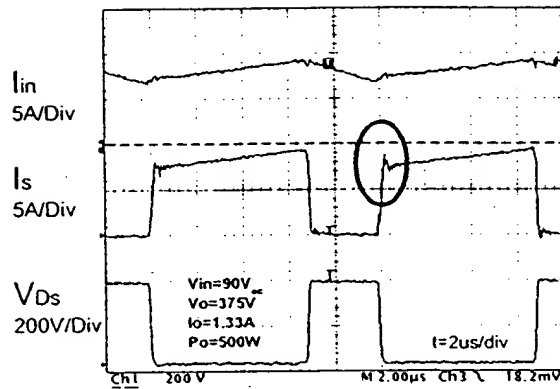


FIG. 16

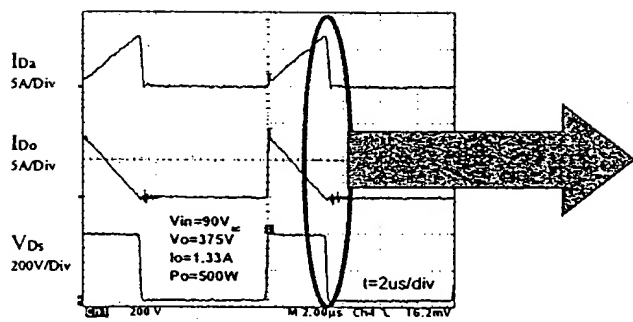


FIG. 17A

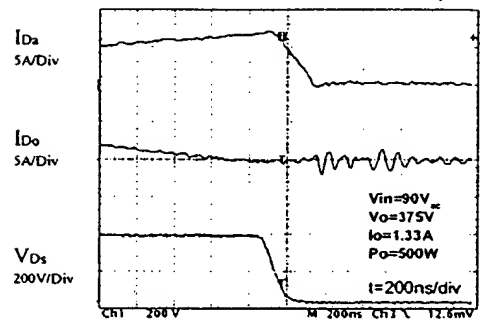


FIG. 17B

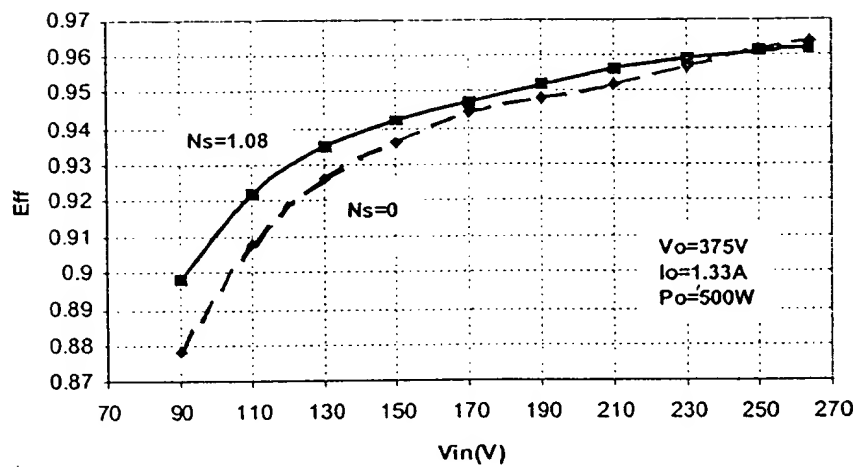


FIG. 18

154 →

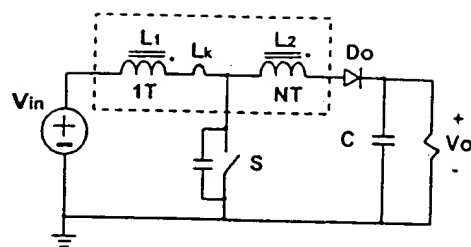


FIG. 19

(PRIOR ART)

FIG. 20

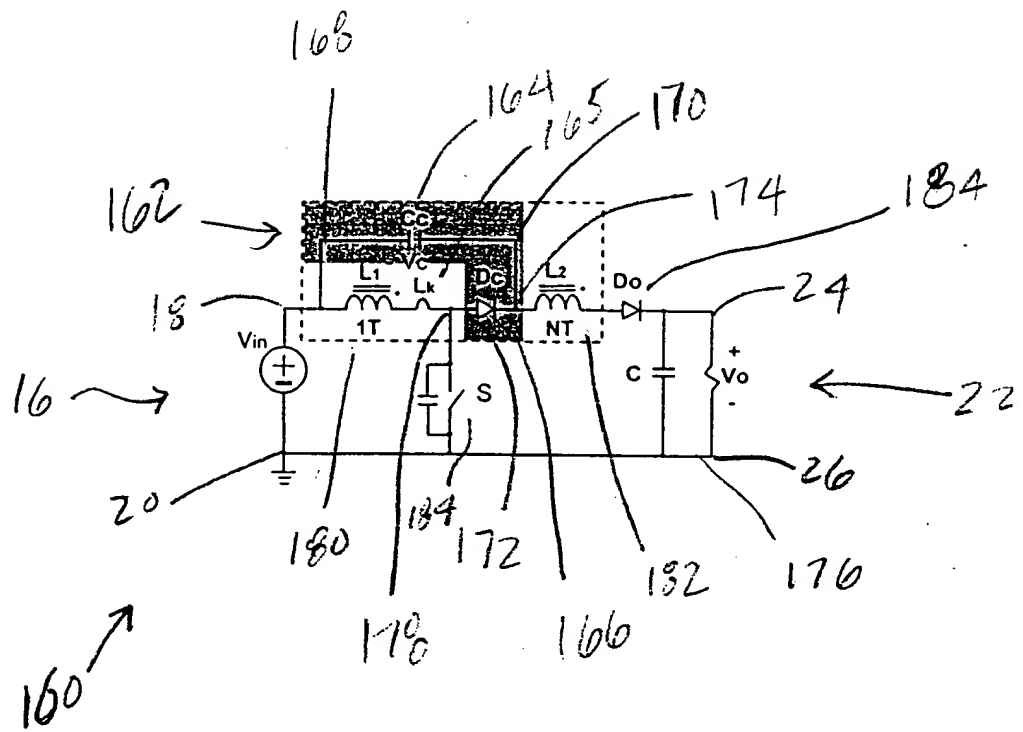


FIG. 20

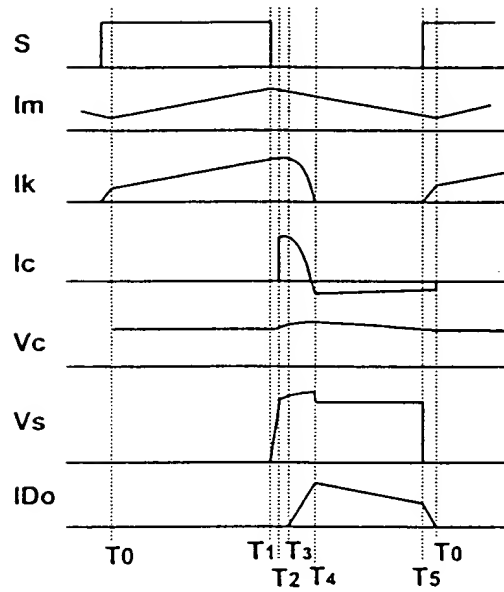


FIG. 21

FIG. 22A

FIG. 22B

FIG. 22C

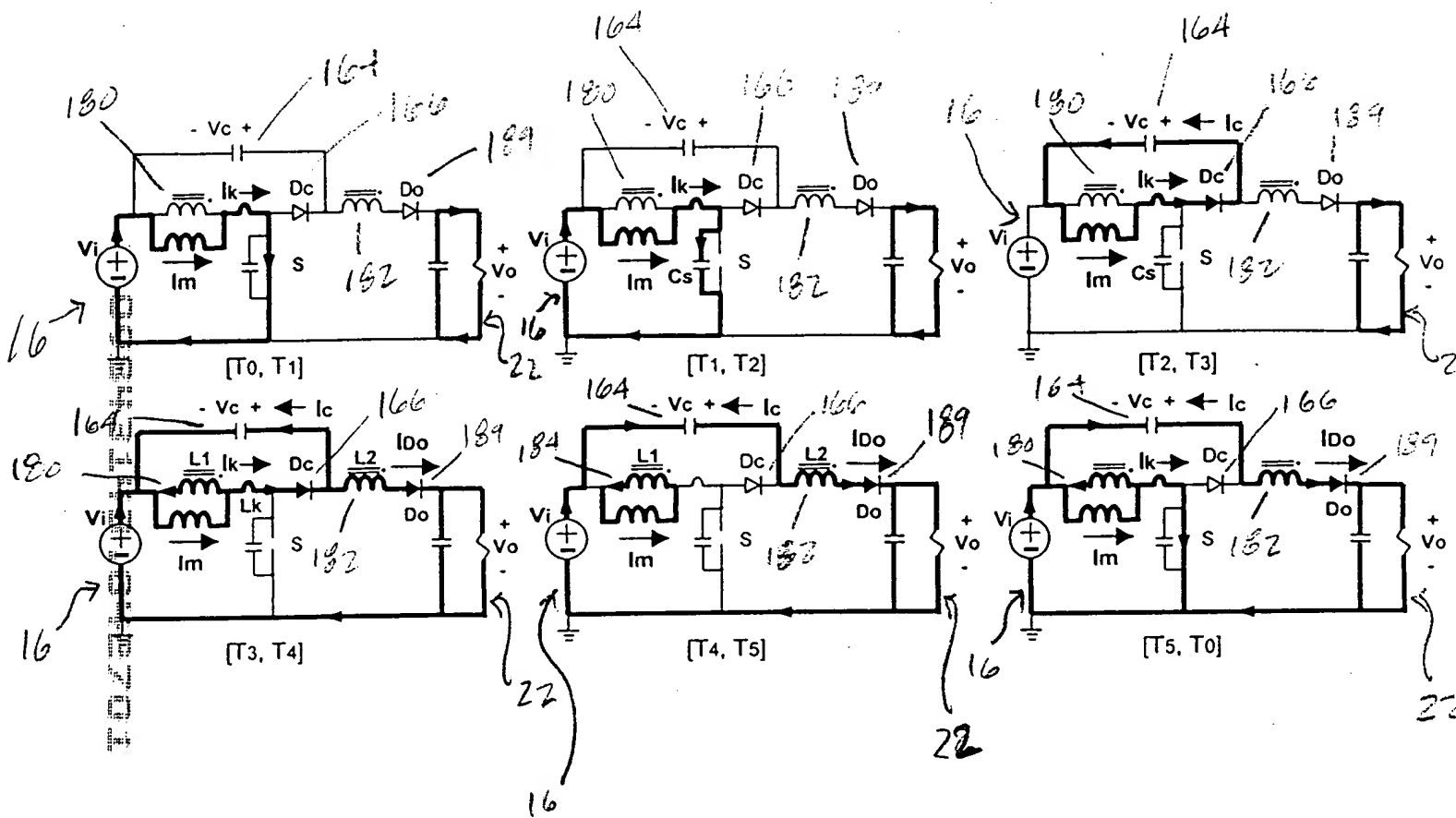


FIG. 22D

FIG. 22E

FIG. 22F

FIG. 23

CONFIDENTIAL

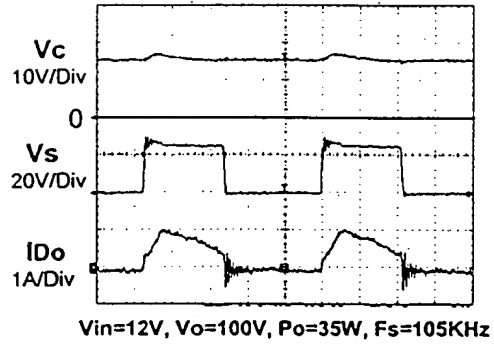
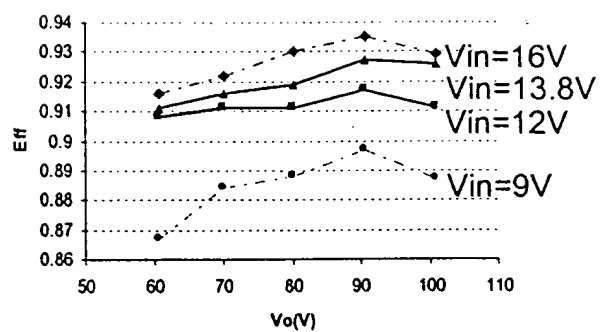


FIG. 24



F16.25

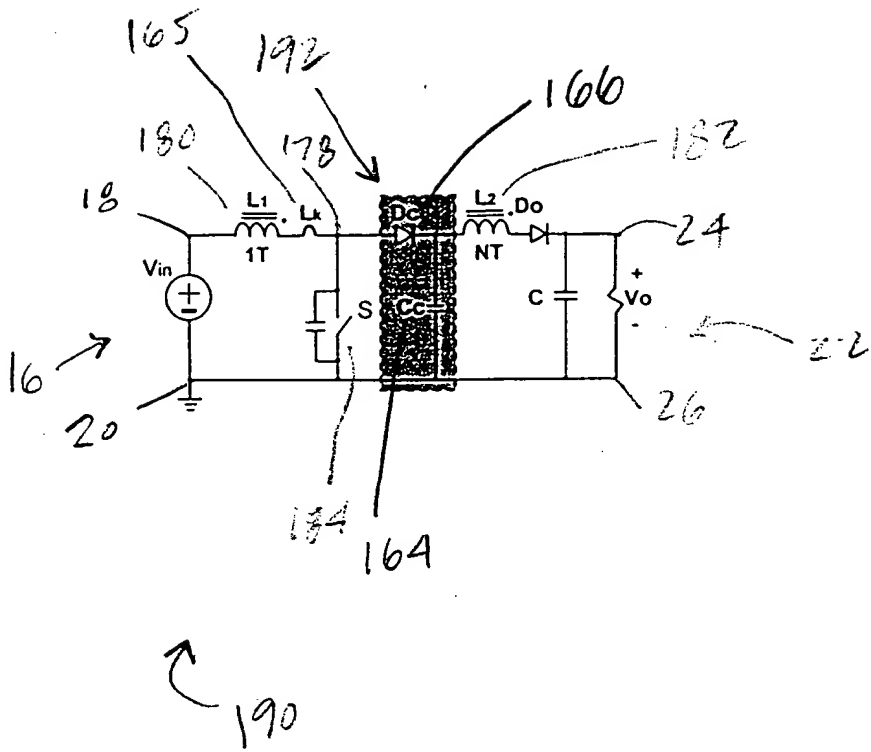
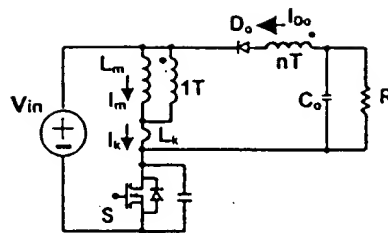


FIG. 26



194 ↗

FIG. 27
(PRIOR ART)

FIG. 28

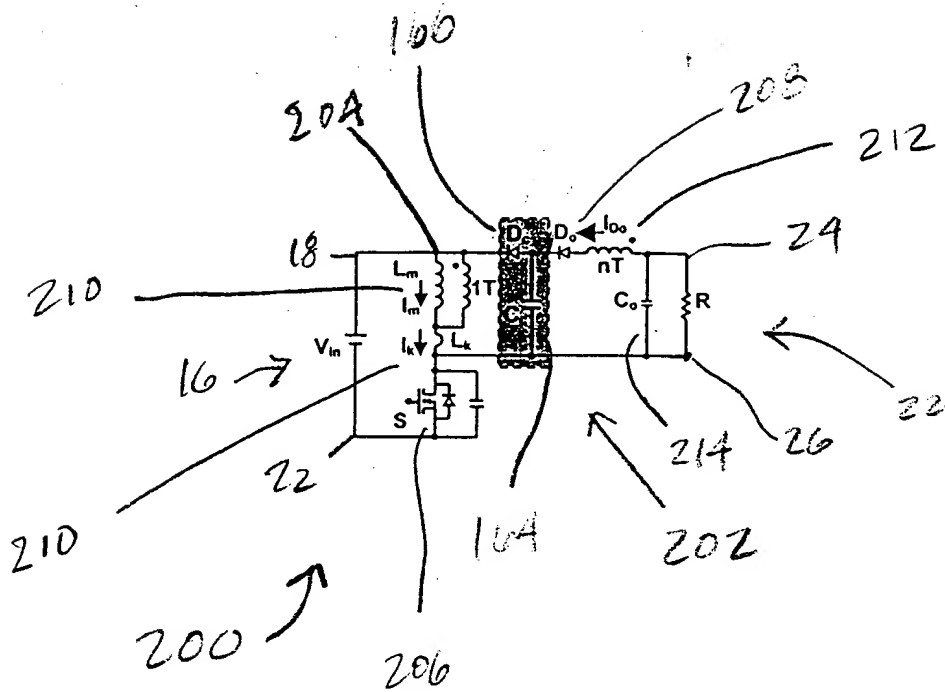


FIG. 28

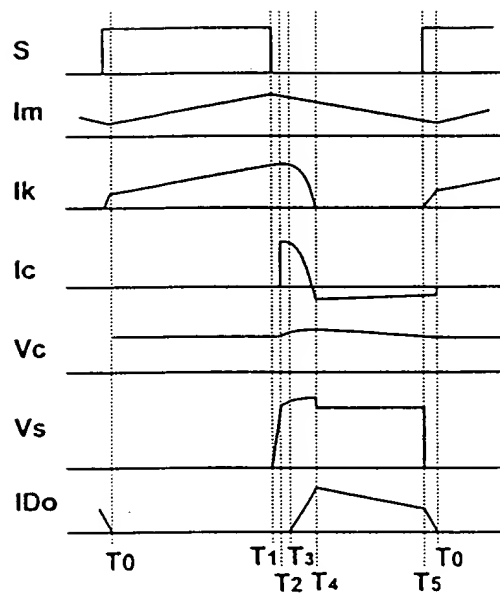


FIG. 29

FIG. 30A

FIG. 30B

FIG. 30C

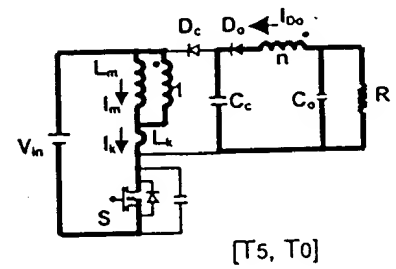
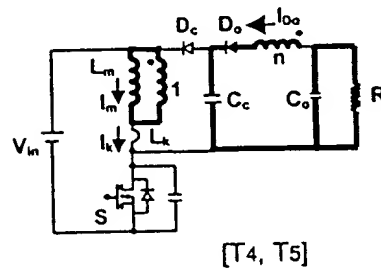
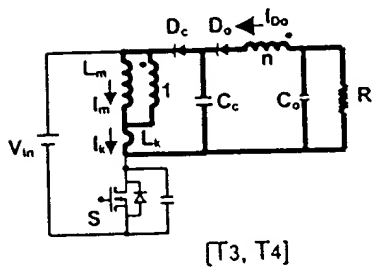
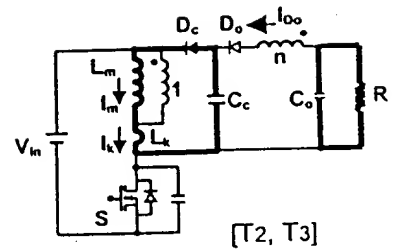
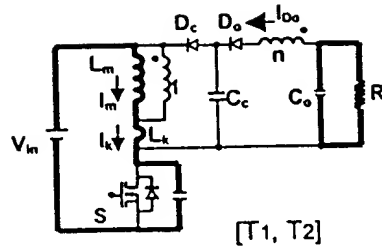
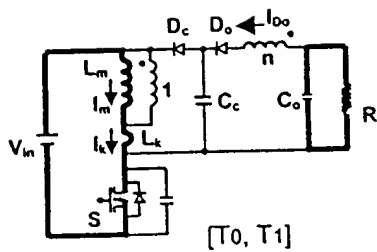


FIG. 30D

FIG. 30E

FIG. 30F

Timing diagram for the 74VHC04 inverter circuit. The diagram shows eight waveforms:

- S**: Clock signal (square wave).
- I_K** : Input current (square wave).
- I_{Cc}** : Input capacitance current (square wave).
- V_{Cc}** : Input voltage (square wave).
- V_S** : Output voltage (square wave).
- I_{Do}** : Output current (square wave).
- I_{Vin}** : Input current (square wave).
- V_O** : Output voltage (square wave).

The time scale is 15.00100000ms, and the voltage scale is 15.01000000ms.

FIG. 31

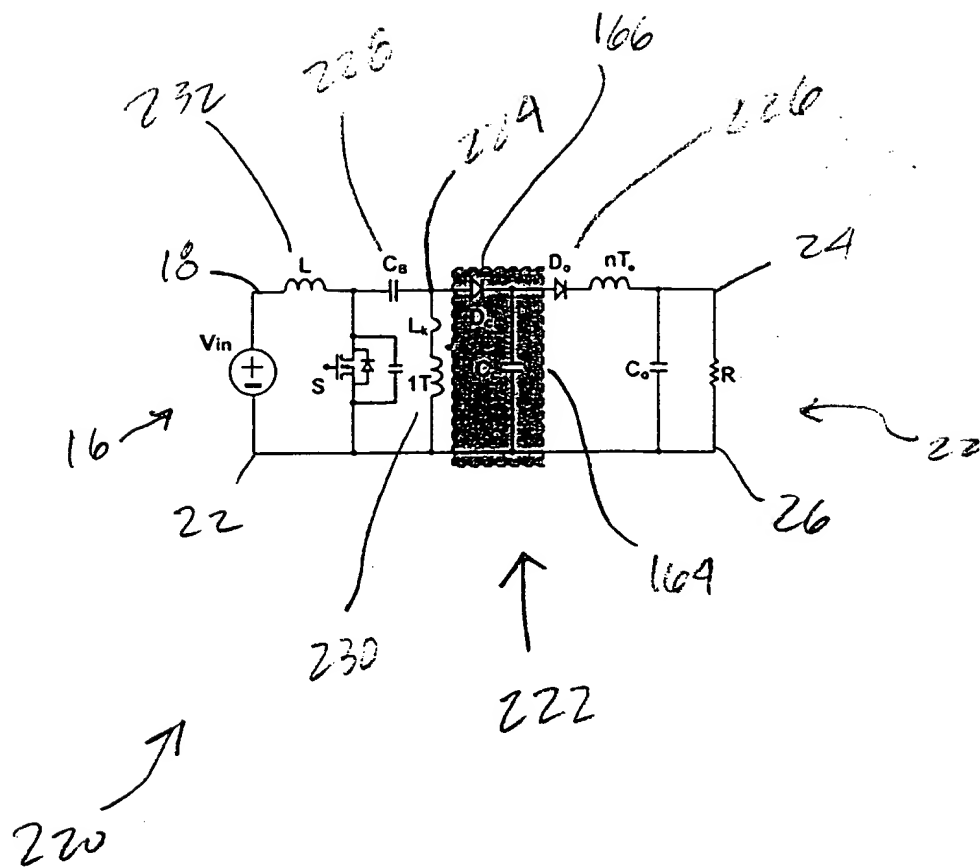


FIG. 33

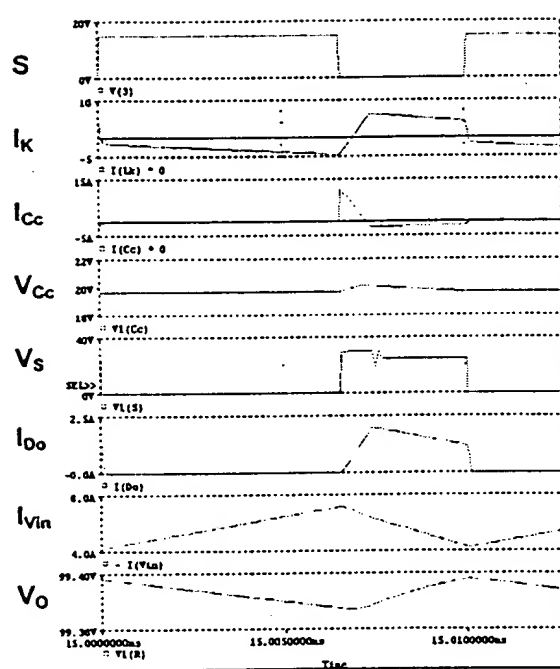


FIG. 34